

Electronic Acknowledgement Receipt

EFS ID:	3056918
Application Number:	09943512
International Application Number:	
Confirmation Number:	8183
Title of Invention:	Multilayer wiring board and semiconductor device
First Named Inventor/Applicant Name:	Yasuo Osone
Customer Number:	20457
Filer:	Gregory Edwards Montone/David Snyder
Filer Authorized By:	Gregory Edwards Montone
Attorney Docket Number:	500.40530X00
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Extension of Time	40530X00EOT.pdf	32465 1a0517c467bdf23b14451b9bb30ced1b616aa0b3	no	1
Warnings:					
Information:					
2	Amendment - After Non-Final Rejection	40530X00AMD.pdf	20213 612775eb58216c805170ebe51d0d8d1c65e452ff	no	1
Warnings:					
Information:					
3	Claims	40530X00CLMS.pdf	517591 256230d0ff387328299f0b5e006c96313d909f16	no	14
Warnings:					
Information:					
4	Applicant Arguments/Remarks Made in an Amendment	40530X00REM.pdf	118489 a7529901d4cf6d069ba869ad8e76d9a80fd43c13	no	3
Warnings:					
Information:					
5	Fee Worksheet (PTO-06)	fee-info.pdf	8158 96b4c88b8f35ba0196913bb4f2edb0a6fa780354	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			696916		
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